

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	81559	clock near5 (frequency or speed)	USPA T; US-P GPUB	2004/08/3 0 15:42	
2	BRS	L2	35190 2	(vary\$3 or chang\$3 or reduc\$3) near5 (frequency or speed)	USPA T; US-P GPUB	2004/08/3 0 15:45	
3	BRS	L3	33234	1 and 2	USPA T; US-P GPUB	2004/08/3 0 15:45	
4	BRS	L4	5367	processor near3 core	USPA T; US-P GPUB	2004/08/3 0 15:46	
5	BRS	L5	17872	multiprocess\$3	USPA T; US-P GPUB	2004/08/3 0 15:47	
6	BRS	L6	25665	(multiple or plurality) adj3 processor\$1	USPA T; US-P GPUB	2004/08/3 0 15:48	
7	BRS	L7	1907	4 and (5 or 6)	USPA T; US-P GPUB	2004/08/3 0 15:48	
8	BRS	L8	203	3 and 7	USPA T; US-P GPUB	2004/08/3 0 15:48	
9	BRS	L9	14003 3	power near3 consum\$5	USPA T; US-P GPUB	2004/08/3 0 15:49	
10	BRS	L10	113	8 and 9	USPA T; US-P GPUB	2004/08/3 0 15:49	
11	BRS	L11	1897	713/300.ccls. 713/320.ccls.	USPA T; US-P GPUB	2004/08/3 0 15:50	
12	BRS	L12	999	713/322.ccls. 713/323.ccls.	USPA T; US-P GPUB	2004/08/3 0 15:50	
13	BRS	L13	1464	713/500.ccls. 713/501.ccls. 713/601.ccls.	USPA T; US-P GPUB	2004/08/3 0 15:51	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	3884	11 or 12 or 13	USPA T; US-P GPUB	2004/08/3 0 15:51	
15	BRS	L15	41	10 and 14	USPA T; US-P GPUB	2004/08/3 0 15:52	

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